FYIT/SEM II/REGULAR/FMPMC

| Time: 2½ hrs. | Mai | rks |
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| Note: | All questions are compulsory with internal options. Figures to the right indicate full marks. | |
| (a) Draw (b) An acceleration (c) Explai (d) Explai i. (e) Explai | er the following (any three) pin diagram of 8085 microprocessor. cumulator contains data 95 and register B contains data AB what will be the of flag register after execution of ADD B instruction. n Stack pointer and program counter. n following pins ALE ii. INTR iii. AD7 – AD0 iv. X1-X2 v. HOLD n Flag register of 8085 in detail. a note on Hardware interrupts also give their vector addresses. | (1 |
| (a) Explain (b) Explain (c) List an (d) Draw t (e) Write a memor | er the following (any three) In LDA address / STA address instructions . In Register addressing mode and Immediate addressing mode in detail. In five instructions of Data transfer group and Arithmetic group each. Iming diagram for Opcode fetch machine cycle. In assembly program to find 2's complement of an 8-bit Number stored at I'y location D001. Store the result in memory location D002. In any three logical instructions. | (1 |
| (a) Write a a) M (b) Draw ti (c) Calcula Delay : M Back: (d) Explain | r the following (any three) ddressing modes of the following instructions. MOV A,B b) ADD M c) LXI H,D001 d) LDA 5001 e)RRC ming diagram for memory read machine cycle. te time delay for given subroutine. MVI B , 25 _H DCR B INZ Back RET Compare instructions. RRC and RLC instructions in detail. | (: |
| (f) Write a | n assembly language program to divide two 8bit , numbers stored at / locations D001 and D002 , store quotient at D003 and remainder at | |
| 4 Answer | the following (any three) | (|

- (a) Explain different available conditional loops along with instructions in the assembly language programming of 8085.
- (b) Explain RIM and SIM instructions in detail.
- (c) Design 8085 system having 8 KB EPROM using 4 KB chips and 16 KB RAM using 8 KB chips.
- (d) Explain CALL and RET instructions with diagram.

- (e) Write an assembly language program to add two 8 bit numbers stored at memory location D001 & D002, store the answer at memory location D003 (Hex code of the program is not expected)
- (f) Write a note on PUSH and POP instructions.

Q.5 Answer the following (any three)

- (a) Write features of 8051 Microcontroller.
- (b) Draw Pin configuration of 8051 Microcontroller.
- (c) Explain alternate functions of port 3 of 8051.
- (d) Explain memory organization of 8051 microcontroller.
- (e) Explain with diagram EDLC.
- (f) Write a note on IDE with it's components.

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(15)